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December 12, 2006

**AMENDMENT AFTER
ALLOWANCE**

Atty Docket No. :	JCLA12240-R2
Appl. No. :	10/826,805
Filing Date :	April, 16, 2004
Pages :	Cover + 4

BY FACSIMILE ONLY

Fax No. :	571-273-8300
Attention :	EXAMINER : NADAV, ORI
Group Unit :	2811
From :	Jiawei Huang, Reg. No. 43,330
MESSAGE :	Enclosed herewith are: [x] Communication [x] Copy of the stamped return postcard, transmittal letter and first page of the priority document as submitted.

Sir:

I hereby certify that this correspondence is being facsimile transmitted to the Patent and Trademark Office on **December 12, 2006** at the above indicated fax number.

Sign by: Michelle Chang
Michelle Chang

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Docket No.: JCLA12240

PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of :

Applicant : SHIH-CHANG SHEI et al.
Application No. : 10/826,805
Filed : April 16, 2004
For : FLIP-CHIP LIGHT EMITTING DIODE PACKAGE STRUCTURE

COMMUNICATION

Assistant Commissioner for Patents
Washington, D.C. 20231

Dear Sir:

The Notice of Allowability regarding the above-identified application indicated that acknowledgement is made of a claim for foreign priority, but None of the certified copies of the priority documents have been received by the PTO.

However, our record shows that the certified copy of Taiwan application No. 92129869 was sent to the PTO on January 18, 2006, and the stamped return post card confirmed that the PTO received the certified copy on January 24, 2006. As evidence, enclosed herewith are copies of the stamped return post card, the transmittal letter, and the first page of the certified copy of Taiwan application no. 92129869 to show that the certified copy indeed was sent to and received by the PTO.

As shown above, Applicant has timely filed the certified copy. Please correct your record and acknowledge the receipt of the certified copy of Taiwan Application No. 92129869.

Respectfully submitted,
J.C. PATENTS

By: 

Jiawei Huang
Registration No. 43,330

Dated: 12/12/2006

Please send correspondence to:
4 Venture, Suite 250
Irvine, California 92618
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(UTILITY / DESIGN PATENT)

PRIORITY DOCUMENTDate : January 18, 2006

CERTIFIED MAIL: 7004 1160 0000 3004 3728

Received in the U.S. Patent and Trademark Office on the date stamped hereon via

Docket #: JCLA12240-R Appl. No.: 10/826,805 Filed on: April 16, 2004

Examiner: NADAV, ORI

Art Unit: 2811

Applicant(s): SHIH-CHANG SHEI et al.

Title: FLIP-CHIP LIGHT EMITTING DIODE PACKAGE STRUCTURE**PLEASE DATE STAMP AND RETURN**

- ☒ Transmittal Sheet
☒ Certified Copy of Taiwan Application No. 92129869 filed on October 28, 2003
☒ Return Postcard

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PATENT

Docket No. JCLA12240-R

page 1

IN THE UNITED STATE PATENT AND TRADEMARK OFFICE

In re Application of : SHIH-CHANG SHEI et al.
Application No. : 10/826,805
Filed : April 16, 2004
For : FLIP-CHIP LIGHT EMITTING DIODE
PACKAGE STRUCTURE

Certificate of Mailing

I hereby certify that this correspondence and all marked attachments are being deposited with the United States Postal Service as certified first class mail in an envelope addressed to: Commissioner for Patents, P.O. BOX 1450, Alexandria VA 22313-1450, on

January 18, 2006

(Date)


Jiawei Huang, Reg. No. 43,330

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

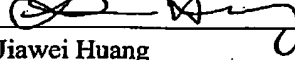
Sir:

Transmitted herewith is a certified copy of Taiwan Application No. 92129869 filed on October 28, 2003.

A return prepaid postcard is also included herewith.

It is believed no fee is due. However, the Commissioner is authorized to charge any fees required, including any fees for additional extension of time, or credit overpayment to Deposit Account No. 50-0710 (Order No. JCLA12240).

Date: 1/18/2006

By: 
Jiawei Huang
Registration No. 43,330

Please send future correspondence to:
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